



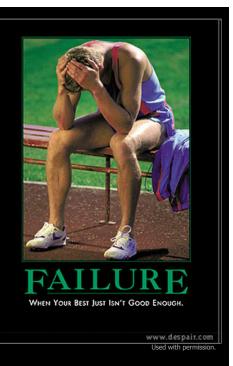




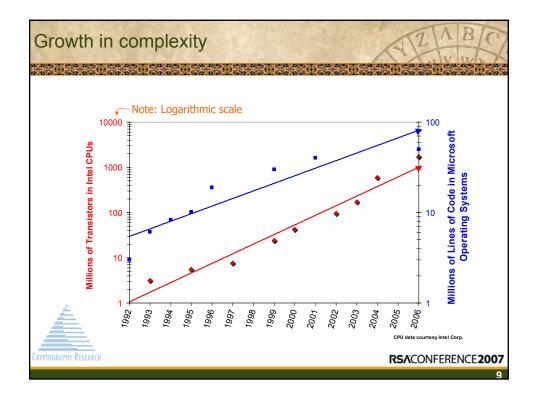
What if we're doomed to an eternity of

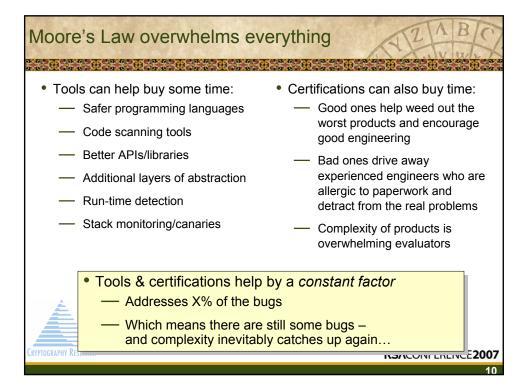
Patches

and other coping mechanisms?



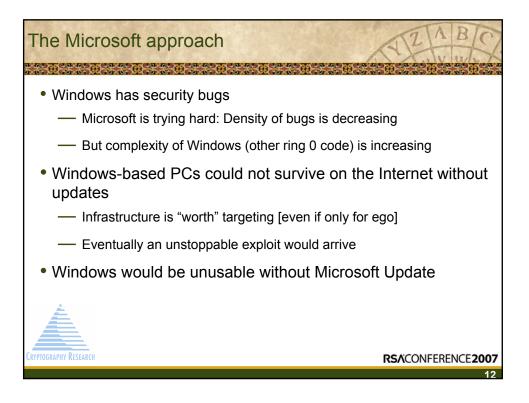




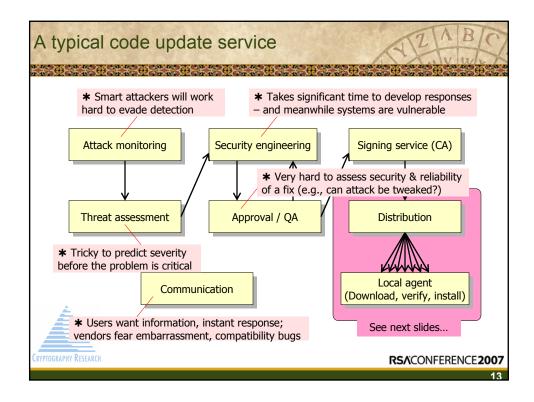


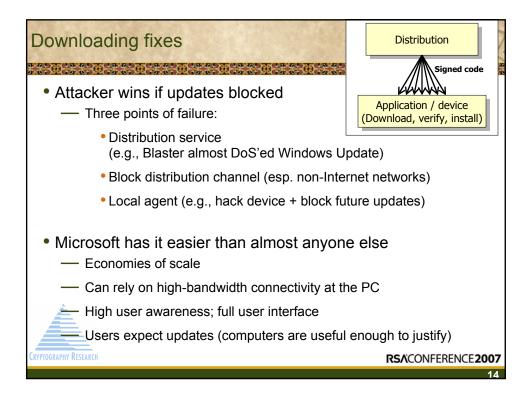




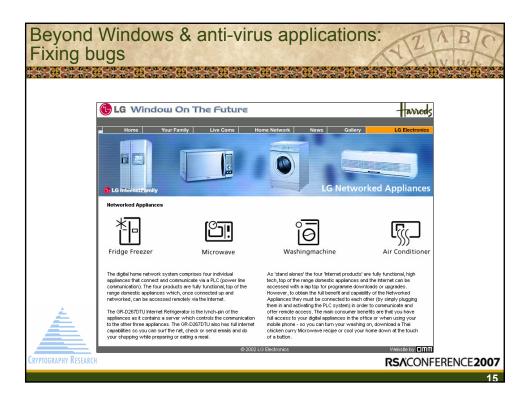


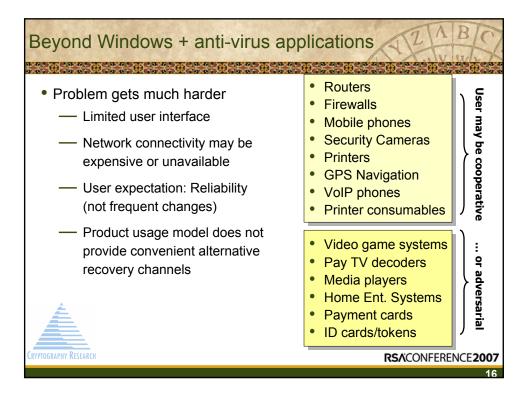




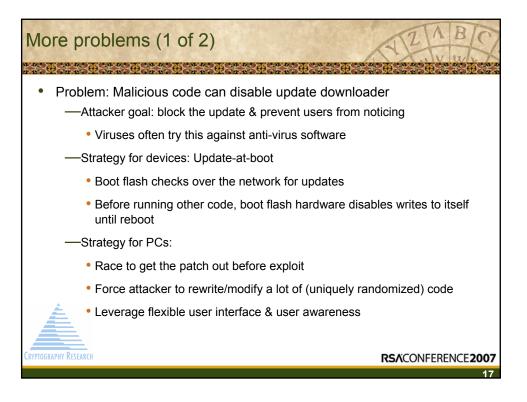


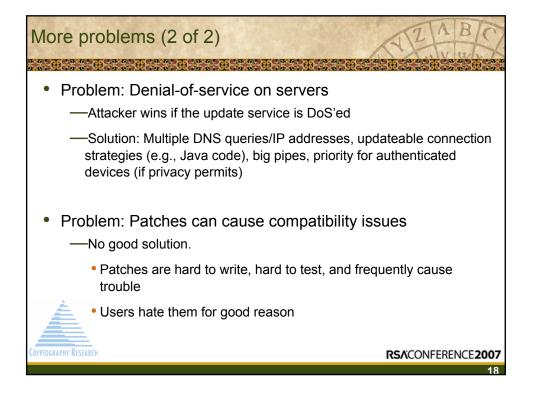




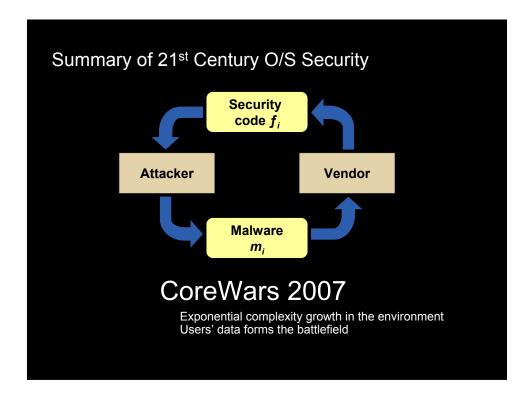


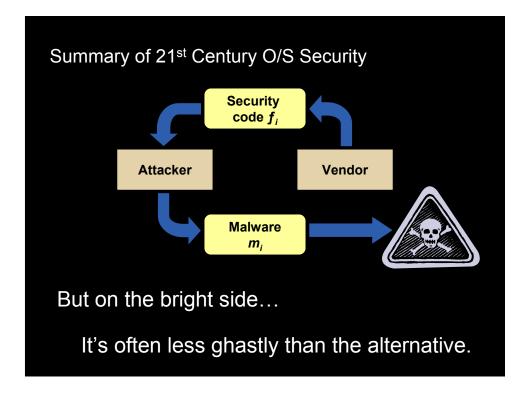


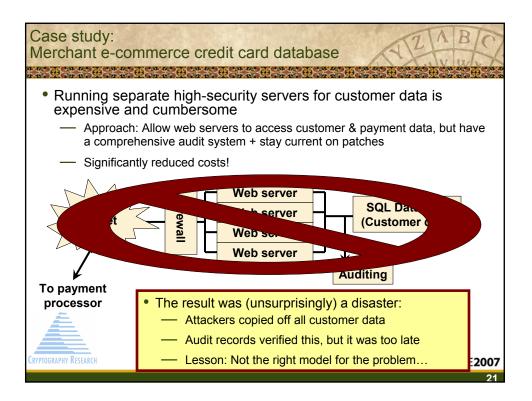


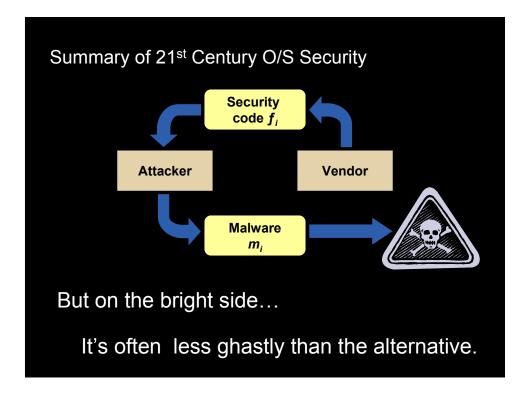


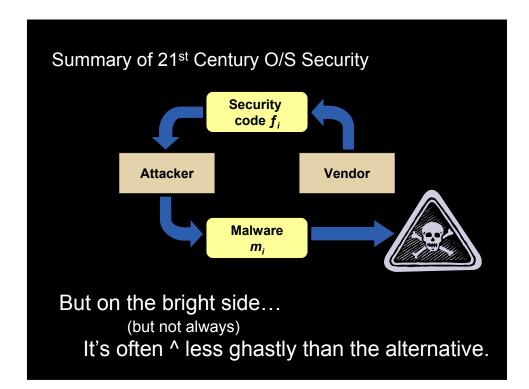


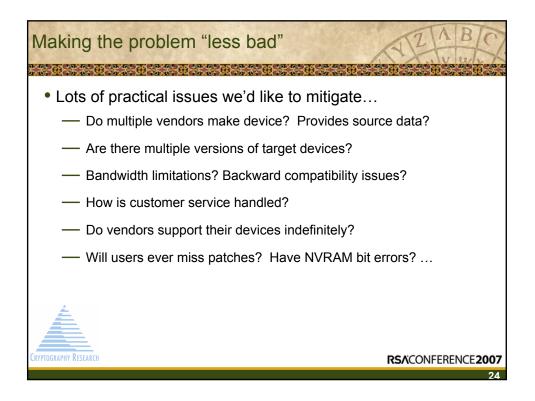




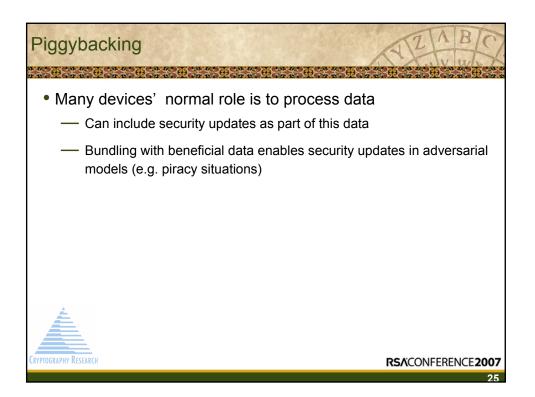


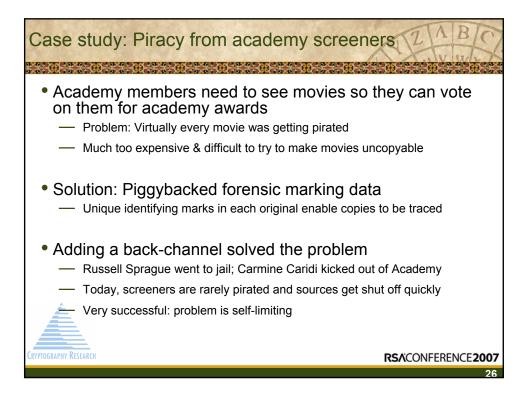




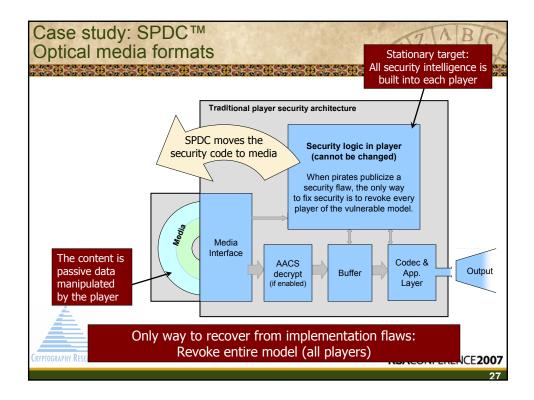


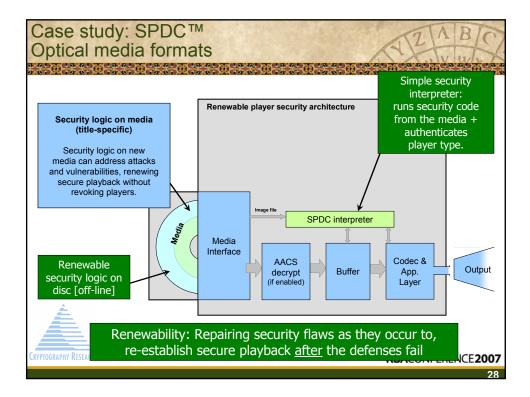




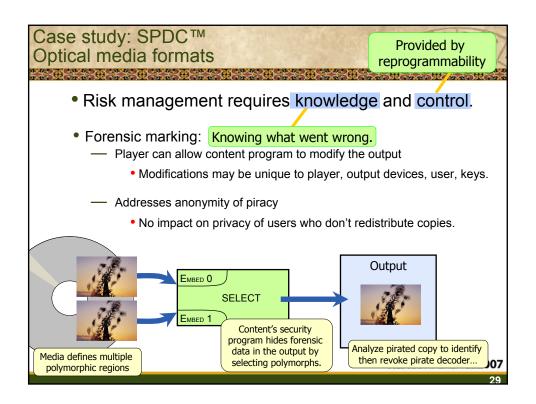


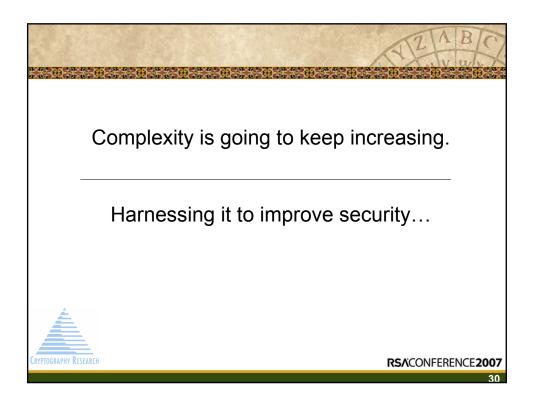








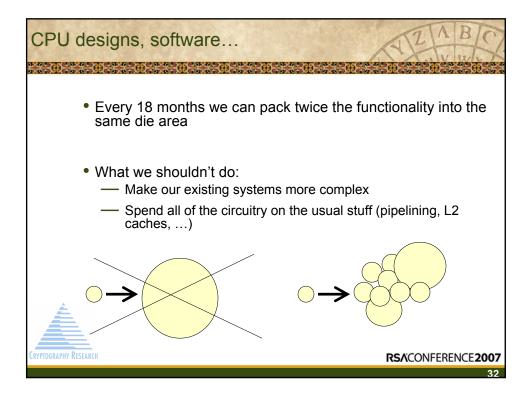




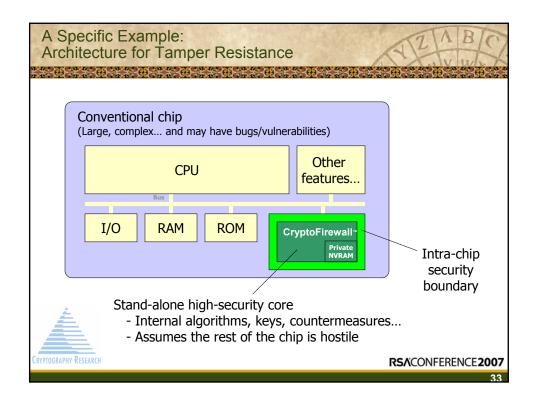


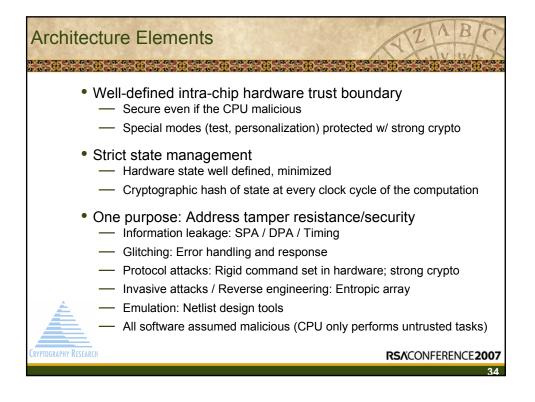
Copyright © 2006-2007 Cryptography Research, Inc.

Hardware architectures
 Fully-featured CPUs (such as those in smart cards) are fiendishly difficult to secure from tampering — Software bugs
 External monitoring attacks Invasive attacks
 A tiny oversight and the whole thing collapses A better general approach is required especially as complexity increases
CRYPTOGRAPHY RESEARCH RSACONFERENCE 2007

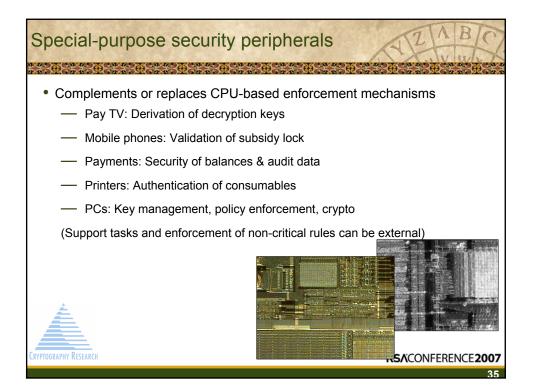


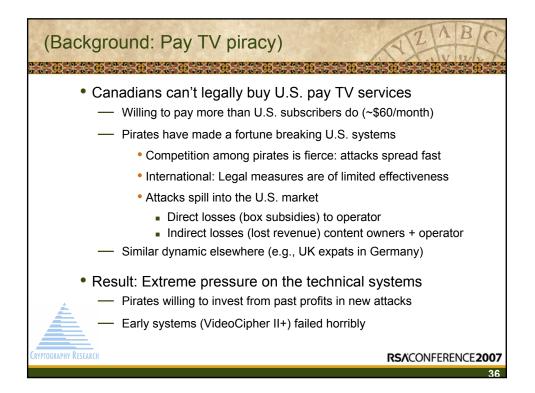






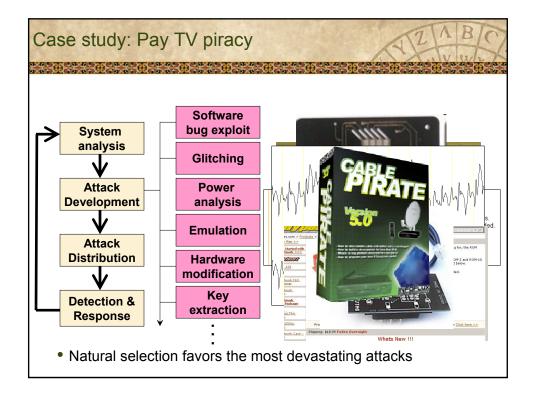




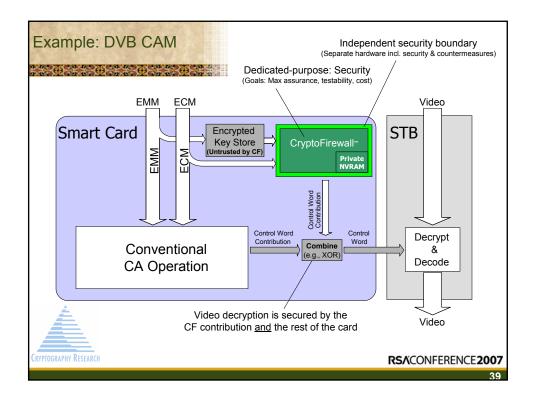


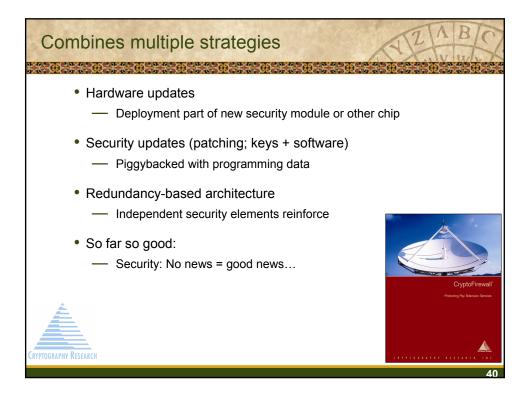




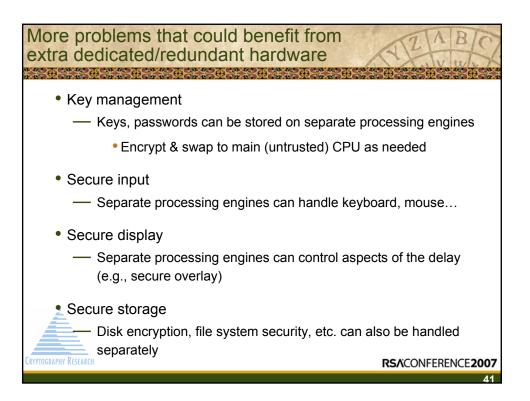


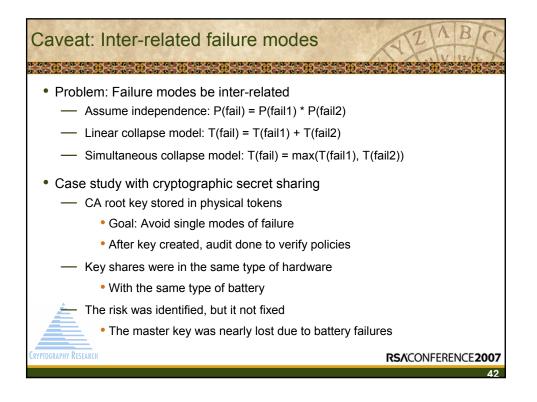




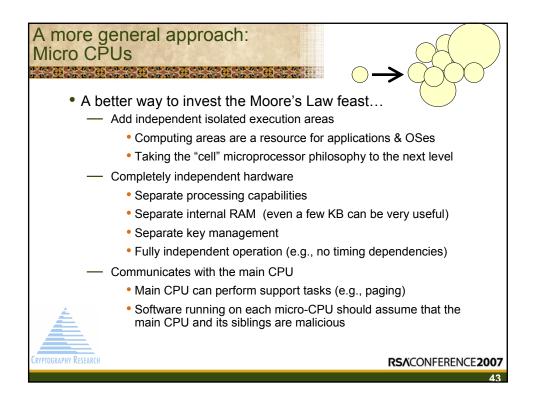


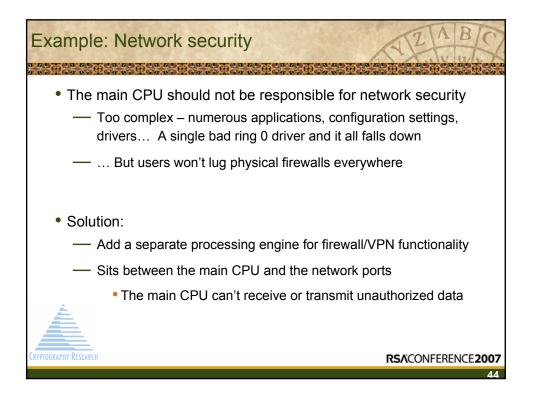




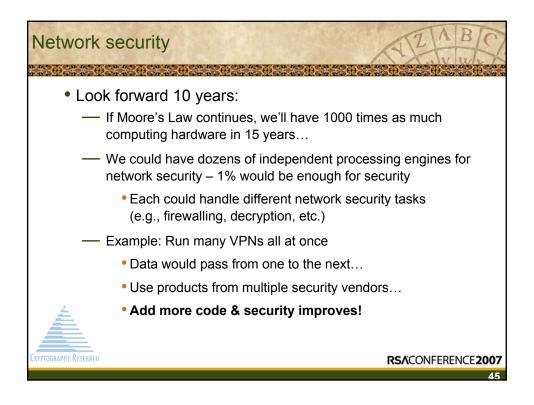
















Questions?			
	Paul Kocher paul@cryptography.com Cryptography Research, Inc. 575 Market St., 21 st Floor San Francisco, CA 94105 USA www.cryptography.com Tel: +1 (415) 397-0123 Fax:+1 (415) 397-0127		
We're hiring			
Interested in the intersection of major real-world security problems and research? Ask me, or send e-mail to jobs@cryptography.com.			

